

AMENDMENTS TO THE CLAIMS

Please **CANCEL** claims 2, 3, 28 and 37 without prejudice or disclaimer.

The following is a complete list of all claims in this application.

1. (Previously Presented) A plasma display panel comprising:
a front glass substrate and a rear glass substrate coupled to each other by a sealing material coated at edges of said front and rear glass substrates;
first and second electrodes on opposing inner surfaces of said front and rear glass substrates so as to cross each other;
a dielectric layer on each of the opposing inner surfaces of said front and rear glass substrates so as to cover said first and second electrodes;
partitions formed on an upper surface of said dielectric layer of said rear glass substrate and extending lengthwise in a first direction wherein the partitions at least partially define a space further defined by one of the first electrodes and one of the second electrodes;
red, green and blue fluorescent substances coated between adjacent ones of said partitions; and
a zone defined between an outermost one of said partitions and the sealing material is at least partially filled with a filling material used for one of said partitions and said zone has a length in the first direction which is substantially a length of the outermost partition in the first direction and at least one surface of the filling material is in direct contact with an outermost surface of the outermost partition along the length of said zone.

2 - 3. (Cancelled)

4. (Previously Presented) A plasma display panel comprising:
a front glass substrate and a rear glass substrate coupled to each other by a sealing
material coated at edges of said front and rear glass substrates;
first and second electrodes on opposing inner surfaces of said front and rear glass
substrates so as to cross each other;
a dielectric layer on each of the opposing inner surfaces of said front and rear glass
substrates so as to cover said first and second electrodes;
partitions formed on an upper surface of said dielectric layer of said rear glass substrate,
red, green and blue fluorescent substances coated between adjacent ones of said
partitions; and
a non-light emitting zone filling portion filling a non-light emitting zone defined between
an outermost one of said partitions and the sealing material, said non-light emitting zone filling
portion comprising a material used for one of said partitions, wherein said non-light emitting
zone filling portion covers at least one end portions of said first electrodes formed on the front
glass substrate.

5. (Previously Presented) A plasma display panel comprising:
a front glass substrate and a rear glass substrate coupled to each other by a sealing
material coated at edges of said front and rear glass substrates;
first and second electrodes on opposing inner surfaces of said front and rear glass
substrates so as to cross each other;

a dielectric layer on each of the opposing inner surfaces of said front and rear glass substrates so as to cover said first and second electrodes;

partitions formed on an upper surface of said dielectric layer of said rear glass substrate, red, green and blue fluorescent substances coated between adjacent ones of said partitions; and

a non-light emitting zone filling portion filling a non-light emitting zone defined between an outermost one of said partitions and the sealing material, said non-light emitting zone filling portion comprising a material used for one of said partitions, wherein a gas exhaust hole is formed at an upper surface of said non-light emitting zone filling portion parallel to a lengthwise direction of said outermost partition.

6. (Original) The plasma display panel as claimed in claim 5, wherein a depth of the gas exhaust hole formed at the upper surface is within a range of 10 μm through 160 μm .

7. (Previously Presented) A plasma display panel comprising:

a front glass substrate and a rear glass substrate coupled to each other by a sealing material coated at edges of said substrates;

first and second electrodes formed on opposing inner surfaces of said front and rear glass substrates so as to cross each other;

a dielectric layer formed on each of the opposing inner surfaces of said front and rear glass substrates to cover said first and second electrodes;

partitions formed on an upper surface of said dielectric layer of said rear glass substrate;

red, green and blue fluorescent substances coated between adjacent ones of said partitions; and

a non-light emitting zone filling portion filling a non-light emitting zone between an outermost one of said partitions and the sealing material, said non-light emitting zone filling portion being disposed adjacent to said outermost partition and comprising a material used for one of said partitions,

wherein

an empty space is defined between the sealing material and said non-light emitting zone filling portion, and

said non-light emitting zone filling portion covers at least one end portion of said first electrodes.

8. (Original) The plasma display panel as claimed in claim 7, wherein a width of said non-light emitting zone filling portion is equal to a length of the end portions of said first electrodes which extend past said outermost partition.

9. (Original) The plasma display panel as claimed in claim 7, wherein a width of said non-light emitting zone filling portion is greater than a length of the end portions of said first electrodes which extend past said outermost partition.

10. (Original) The plasma display panel as claimed in claim 9, wherein a sum of the width of said non-light emitting zone filling portion and a width of said outermost partition is

1.0 mm, and a length of the end portion of each of said first electrodes covered by said non-light emitting zone filling portion and said outermost partition is 0.3 mm.

11. (Original) The plasma display panel as claimed in claim 7, wherein said first electrodes extend past said non-light emitting zone filling portion where a width of the empty space is less than 50 μm .

12. (Original) The plasma display panel as claimed in claim 7, wherein a gas exhaust hole is formed on an upper surface of said non-light emitting zone filling portion parallel to a lengthwise direction of said outermost partition.

13. (Original) The plasma display panel as claimed in claim 12, wherein a depth of the gas exhaust hole from the upper surface is between 10 μm and 160 μm .

14. (Cancelled)

15. (Previously Presented) A plasma display panel, comprising:
a front glass substrate having first electrodes over which a first dielectric layer is formed;
a rear glass substrate disposed opposite said front glass substrate, said rear glass substrate having second electrodes over which a second dielectric layer is formed, the second electrodes not being parallel with the first electrodes;
a seal connecting corresponding edges of said front and rear glass substrates;

partitions formed on an upper surface of the second dielectric layer between the edges of said rear glass substrate;

a fluorescent substance coated between said partitions; and

a non-light emitting zone filling portion disposed between an outermost one of said partitions and said seal so as to prevent a discharge of the first electrodes in a space between said outermost partition and said seal, wherein said seal is disposed such that at least one of opposing ends of each of the first electrodes is disposed between said seal and said partitions.

16. (Original) The plasma display panel of claim 15, wherein said outermost partition and said non-light emitting zone filling portion comprise the same material.

17. (Previously Presented) A plasma display panel, comprising:

a front glass substrate having first electrodes over which a first dielectric layer is formed;

a rear glass substrate disposed opposite said front glass substrate, said rear glass substrate having second electrodes over which a second dielectric layer is formed, the second electrodes not being parallel with the first electrodes;

a seal connecting corresponding edges of said front and rear glass substrates;

partitions formed on an upper surface of the second dielectric layer between the edges of said rear glass substrate;

a fluorescent substance coated between said partitions; and

a non-light emitting zone filling portion disposed between an outermost one of said partitions and said seal so as to prevent a discharge of the first electrodes in a space between said outermost partition and said seal, wherein said non-light emitting zone filling portion is

connected to and has a same height as said outermost partition and at least one surface of the non-light emitting zone portion is in contact with the dielectric layer on the front glass substrate or the dielectric layer on the rear glass substrate along a width of the non-light emitting zone portion from an outermost surface of the outermost partition to an innermost surface of the sealing material and another surface of the non-light emitting zone portion is in contact with substantially all of the outermost surface of the outermost partition.

18. (Previously Presented) The plasma display panel of claim 17, wherein said non-light emitting zone filling portion substantially completely fills the space between said outermost partition and said seal.

19. (Previously Presented) A plasma display panel, comprising:
a front glass substrate having first electrodes over which a first dielectric layer is formed;
a rear glass substrate disposed opposite said front glass substrate, said rear glass substrate having second electrodes over which a second dielectric layer is formed, the second electrodes not being parallel with the first electrodes;
a seal connecting corresponding edges of said front and rear glass substrates;
partitions formed on an upper surface of the second dielectric layer between the edges of said rear glass substrate;
a fluorescent substance coated between said partitions; and
a non-light emitting zone filling portion disposed between an outermost one of said partitions and said seal so as to prevent a discharge of the first electrodes in a space between said outermost partition and said seal, wherein each of said first electrodes comprises

a terminal end extending to said seal, and
a non-terminal end that does not extend to said seal, said non-light emitting zone
filling portion and said outermost partition covering the non-terminal end.

20. (Original) The plasma display panel of claim 19, wherein the non-terminal
end extends past said non-light emitting zone filling portion when a width of an empty space
between said seal and said non-light emitting zone filling portion is less than 50 μm .

21. (Original) The plasma display panel of claim 19, wherein the non-terminal
end extends past said outermost partition and into but not through said non-light emitting zone
filling portion.

22. (Original) The plasma display panel of claim 19, wherein the non-terminal
end extends past said outermost partition, through said non-light emitting zone filling portion,
but does not extend into a space defined between said non-light emitting zone filling portion and
said seal.

23. (Original) The plasma display panel of claim 15, wherein said non-light
emitting zone filling portion and the first dielectric layer define a gas removal channel through
which gas is removed from the plasma display panel.

24. (Original) The plasma display panel of claim 15, wherein said non-light emitting zone filling portion and said seal define a gas removal channel through which gas is removed from the plasma display panel.

25. (Original) The plasma display panel of claim 24, wherein the gas removal channel is defined by approximately one half of a space between said outermost partition and said seal.

26. (Original) The plasma display panel of claim 25, wherein the space between said outermost partition and said seal has a width of 20 mm, and the gas discharge channel has a width of 10 mm.

27. (Original) The plasma display panel of claim 23, wherein another gas removal channel is defined between said non-light emitting zone filling portion and said seal.

28 - 37. (Cancelled)